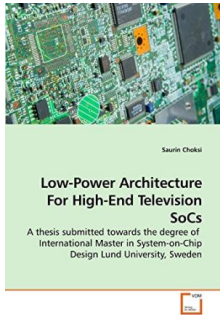


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LOW-POWER ARCHITECTURE FOR HIGH-END TELEVISION SOCS



Condition: New. Publisher/Verlag: VDM Verlag Dr. Müller | A thesis submitted towards the degree of International Master in System-on-Chip Design Lund University, Sweden | This book focuses on the exploration of various power optimization methods for video processing SoCs. An existing application, a video pixel processor for high-end television sets, is used as a case study. Furthermore, the influence of a recently developed communication infrastructure, Network-on-Chip (NoC), on power dissipation is analyzed. An extensive power analysis is performed on the...

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