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IC DESIGN OF CMOS UHF RFID ANALOG FRONT END



IC Design of CMOS UHF RFID
Analog Front End
Circuit design techniques of CMOS UHF RRID AFE
using Dynamic Threshold MOSFET



LAP Lambert Academic Publishing Okt 2016, 2016. Taschenbuch. Condition: Neu. Neuware - The application of ultra high frequency (UHF) radio frequency identification (RFID) has been gaining momentum since the ratification of EPC Class 1 Gen 2 (EPC CIG2, also known as ISO 18000-6C) protocol. The limited availability of Schottky barrier diode (SBD) has stimulated the investigation of alternate circuit solutions to fabricate UHF RFID chips of comparable performance using typical CMOS processes. UHF RFID analog front end has four building...

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- Released at 2016



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