



## Device Modeling in CMOS Integrated Circuits

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Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | Interconnects, Inductors and Transformers | On-chip passive components such as interconnects, inductors and transformers are widely used in CMOS high speed digital, mixed-signal and radio frequency integrated circuits (ICs). Therefore, accurate modeling of circuit behavior, especially for these passive components, is crucial for first-time-right designs. This book focus on modeling and characterization of on-chip interconnects, inductors and transformers. Firstly, a fully scalable and SPICE-compatible interconnects model is established and is accurate over a wideband frequency range up to 110 GHz. Secondly, frequency- and temperature-dependent characteristics of on-chip coupled asymmetrical and symmetrical interconnects are investigated in detail. Furthermore, an eleven-element equivalent circuit model is established for on-chip spiral inductors. Additionally, a vertical tapered solenoidal inductor is designed to achieve a high resonance frequency. Finally, extensive studies on the performances of on-chip transformers with and without patterned ground shields at different temperatures are carried out. This book targets at those who are interested in the CMOS IC designs, and would like to develop passive devices models. | Format: Paperback | Language/Sprache: english | 315 gr | 228 pp.



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