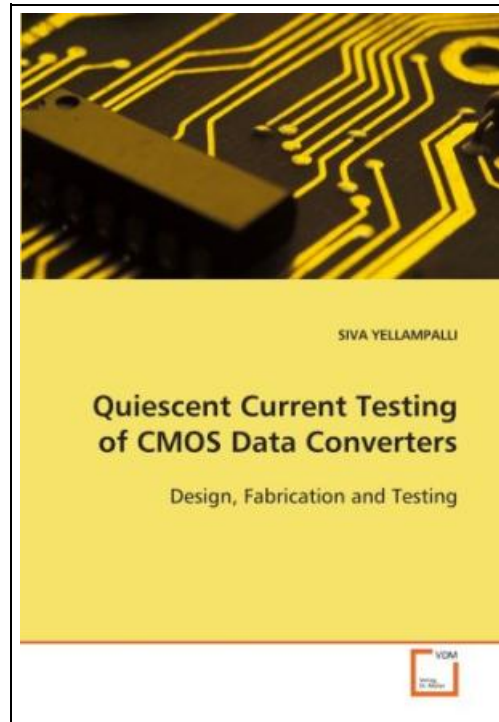


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QUIESCENT CURRENT TESTING OF CMOS DATA CONVERTERS



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VDM Verlag Feb 2009, 2009. Taschenbuch. Book Condition: Neu. 220x150x7 mm. Neuware - 1999 fand anlässlich des 70. Geburtstages von Jürgen Habermas ein Symposium an der Johann Wolfgang Goethe-Universität statt, in dem speziell die Aspekte eines Werkes diskutiert wurden, die das Verhältnis von Rechtfertigung und Wahrheit bzw. Richtigkeit betreffen. Diese Beiträge wurden in dem Band 'Die Öffentlichkeit der Vernunft und die Vernunft der Öffentlichkeit' publiziert. Darin findet sich auch eine kritische Auseinandersetzung des amerikanischen Philosophen Hilary W. Putnam mit der Moraltheorie von Jürgen Habermas. Putnam kritisiert die dichotome Trennung von Werten und Normen in der Diskursethik, aus der letztlich das Problem entsteht, dass entweder die in der Diskursethik generierten Normen keinen universellen Status erlangen könnten oder die Trennung zwischen Wert und Norm selbst unhaltbar sei. Aus dieser Kritik entwickelt sich in der Folgezeit eine Debatte um die Frage nach dem Verhältnis zwischen Norm und Wert. Die vorliegende Arbeit führt in die moralphilosophischen Grundpositionen von Habermas und Putnam ein, stellt die spezifische Problematik der Debatte dar und erörtert einige Lösungsperspektiven. Power supply quiescent current (IDDQ) testing has been very effective in detecting physical defects such as open, short and bridging defects in VLSI circuits designed in CMOS processes. However, in sub-micron VLSI circuits, IDDQ current is masked by the increased subthreshold (leakage) current of MOSFETs affecting the efficiency of IDDQ testing. In this work, an attempt has been made to perform robust IDDQ testing in presence of increased leakage current by suitably modifying some of the test methods normally used in industry. In this book we have re-examined both IDDQ and Delta IDDQ methods of testing CMOS VLSI circuits and added features to minimize the influence of leakage current. We have designed built-in current sensors (BICS) for on-chip testing of analog and mixed-signal integrated circuits. We have also combined quiescent current testing with oscillation and transient current techniques to map large number of manufacturing defects on a chip. 116 pp. Deutsch.



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