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MODELLING OF LOW POWER ACCELEROMETER INTERFACING CIRCUITRY



Swetha Priya A.
Modelling of Low Power
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LAP Lambert Academic Publishing Feb 2017, 2017. Taschenbuch. Condition: Neu. Neuware - The surge for silicon accelerometers with low form factor-g and low noise-power-voltage interfacing readout systems are of paramount interest for the next generation's light-weight, low-cost Inertial Measurement Units (IMU's) and microgravity measurement systems, opening the scope for a wide range of low-power applications in VLSI-MEMS technology. This key-informative book gives a head start for reader to participate in emerging fields of application. It deals with the designing, modelling,...

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- Authored by Swetha Priya A.
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