Synthesis of Sequential Reversible Circuits through State Machine

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Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | A Designers Approach to Realize Sequential Reversible Circuits | Today s computing scenario demands moving beyond conventional way of computing. Some alternatives are addressed to resolve the issues of conventional computing. Out of these alternatives, the reversible computing is emerging as a promising technology that produces high packaging density, computation speed and low power consumption. This book provides a technically rigorous introduction to reversible computing such as reversible logic, library gates, optimization technique and methods for sequential reversible circuit realization. The book discusses the related literature survey on sequential reversible computing. In addition, this text book presents a specialized method for the synthesis of a low cost reversible gate suitable for sequential building block i.e. T flip-flop is proposed. Hence the realization of low cost reversible synchronous and asynchronous counters is reported. A procedure for obtaining reversible circuit from behavioral description through Finite State Machine (FSM) is also incorporated in the text. This work has improved the generation of reversible FSM and evolved the step by step procedure to generate the sequential reversible circuit from reversible FSM. | Format: Paperback | Language/Sprache: english | 84 pp.

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Reviews

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