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## LOGIC MINIMIZATION ALGORITHMS FOR VLSI SYNTHESIS

Logic  
Minimization  
Algorithms for  
VLSI Synthesis

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Springer. Hardcover. Condition: New. 194 pages. Dimensions: 9.4in. x 6.0in. x 0.7in. The roots of the project which culminates with the writing of this book can be traced to the work on logic synthesis started in 1979 at the IBM Watson Research Center and at University of California, Berkeley. During the preliminary phases of these projects, the importance of logic minimization for the synthesis of area and performance effective circuits clearly emerged. In 1980, Richard Newton stirred our interest by...

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